



2816  
#51A  
w/ATTACHMENT  
6/10/03  
Mollai

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Appln. Of: KIMURA

Serial No.: 10/091,776

Filed: March 5, 2002

For: CMOS REFERENCE VOLTAGE CIRCUIT

Group: 2816

Examiner: TERRY LEE ENGLUND

DOCKET: NEC NEG-244

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT A

Dear Sir:

This Amendment is being filed in response to the Official Action mailed

February 28, 2003.

Please amend the Application as follows:

IN THE SPECIFICATION:

Please replace the heading at page 1, line 10, with the following rewritten heading:

A1

--BACKGROUND OF THE INVENTION--

Please replace the paragraph beginning at page 11, line 10, with the following rewritten paragraph:

A2

--In accordance with the present invention, the operational amplifier is driven with the constant current, each of two transistors making up an input differential pair has a gate W/L ratio of 1:K2, and the gate W/L ratio of the two transistors forming an active load operating as a load to the two transistors is K3:1, with offset values being summed together.--

HAYES SOLOWAY P.C.  
130 W. CUSHING ST.  
TUCSON, AZ 85701  
TEL. 520.882.7623  
FAX. 520.882.7643

175 CANAL STREET  
MANCHESTER, NH 03101  
TEL. 603.668.1400  
FAX. 603.668.8567

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